查询IRFK6HE50供应商

Bulletin E27109

International IOR Rectifier IRFK4HE50,IRFK4JE50

Isolated Base Power HEX-pak™ Assembly - Parallel Chip Configuration

- High Current Capability.
- UL recognised E78996.
- Electrically Isolated Base Plate.
- Easy Assembly into Equipment.

Description

The HEX-pak™ utilises the well-proven HEXFET™ die, combining low on-state resistance with high transconductance. These superior technology die are assembled by state of the art techniques into the TO-240 package, featuring 2.5kV rms isolation and solid M5 screw connections. The small footprint means the package is highly suited to power applications where space is a premium. Available in two versions, IRFK.H... for fast switching and IRFK.J... for oscillation sensitive applications.

V_{DS} = 800V

$R_{DS(on)}$ = 300mΩ

I_D = 26A

Absolute Maximum Rating

	Parameter	Max.	Units
I_D @ T_C=25°C	Continuous Drain Current	26	A
I_D @ T_C=100°C	Continuous Drain Current	16	A
I_{DM}	Pulse Drain Current	104	A ①
P_D @ T_C=25°C	Maximum Power Dissipation	500	W
V_{GS}	Gate-to-Source Voltage	20	V
V_{INS}	R.M.S. Isolation Voltage, circuit to base	2.5	kV
T_J	Operating Junction Temperature Range	-40 to 150	°C
T_{STG}	Storage Temperature Range	-40 to 150	°C

Thermal and Mechanical Specifications

	Parameter	Min.	Typ.	Max.	Units
R_{thJC}	Junction-to-Case	-	-	0.25	K/W ②
R_{thCS}	Case-to-Sink, smooth & greased surface	-	0.1	-	K/W
T	Mounting Torque +10%				③
	HEXpak to Heatsink	-	5	-	Nm
	Busbar to HEXpak	-	3	-	Nm
wt	Approximate Weight	-	140	-	g
		-	5	-	oz

Notes:

① - Repetitive Rating: Pulse width limited by maximum junction temperature see figure 8.

② - Per Module.

③ - A mounting compound is recommended and the torque should be rechecked after a period of three hours to allow for the spread of the compound.

Electrical Characteristics @ T_J = 25°C (Unless otherwise specified)

	Parameter		Min.	Typ.	Max.	Units	Test Conditions
B_{VDSS}	Drain-to-Source Breakdown voltage		800	-	-	V	V_{GS}=0V, I_D=1.0mA
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance		-	240	300	mΩ	V_{GS}=10V, I_D=8A
$I_{D(on)}$	On-State Drain Current		26	-	-	A	V_{DS} > $I_{D(on)}$ x $R_{DS(on)}$max, V_{GS}=10V
$V_{GS(th)}$	Gate Threshold Voltage		2.0	-	4.0	V	V_{DS}=V_{GS}, I_D=1.0mA
g_{fs}	Forward Transconductance ④		26	35	-	S	V_{DS} > 50V, I_D=16A
I_{DSS}	Zero Gate Voltage Drain Current		-	-	1.0	mA	V_{DS}=V_{DS}max, V_{GS}=0v
			-	-	4.0	mA	V_{GS}=10V, T_C=125°C, V_{DS}=V_{DS}max x 0.8
I_{GSS}	Gate-to-Source Leakage Forward		-	-	400	nA	V_{GS}=20V
I_{GSS}	Gate-to-Source Leakage Reverse		-	-	-400	nA	V_{GS}=-20V
Q_g	Total Gate Charge		-	580	760	nC	I_D=26A, V_{GS}=10V,
Q_{gs}	Gate-to-Source Charge		-	40	60	nC	V_{DS}=V_{DS}max x 0.8
Q_{gd}	Gate-to-Drain ("Miller") Charge		-	288	440	nC	
$t_{d(on)}$	Turn-on Delay Time	IRFK4HE50	-	70	-	ns	V_{DD}=400V, I_D=16A,
		IRFK4JE50	-	80	-	ns	
t_r	Rise Time	IRFK4HE50	-	55	-	ns	V_{GS}=10V,
		IRFK4JE50	-	70	-	ns	
$t_{d(off)}$	Turn-off Delay Time	IRFK4HE50	-	350	-	ns	R_{SOURCE}=3.3Ω
		IRFK4JE50	-	470	-	ns	
t_f	Fall Time	IRFK4HE50	-	60	-	ns	
		IRFK4JE50	-	100	-	ns	
L_{DS}	Drain-to-Source Inductance		-	18	-	nH	
C_{iss}	Input Capacitance		-	10.5	-	nF	V_{GS}=0V, V_{DS}=25V,
C_{oss}	Output Capacitance		-	2.5	-	nF	f=1.0MHz
C_{rss}	Reverse Transfer Capacitance		-	2.0	-	nF	
V_{INS}	R.M.S. Isolation Voltage		2.5	-	-	kV	Circuit to Base

Source-Drain Diode Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	-	-	26	A	
I_{SM}	Pulsed Source Current (Body Diode)	-	-	90	A	
V_{SD}	Diode Forward Voltage	-	-	1.8	V	V_{GS}=0V, I_S= 26A, T_C=25°C
t_{rr}	Reverse Recovery Time	370	770	1600	ns	di/dt=400A/μs, T_J=150°C
Q_{rr}	Reverse Recovered Charge	12.0	26.0	52.0	μC	I_S=26A

Notes:

④ - Pulse Width ≤ 300μs; Duty cycle ≤ 2%.

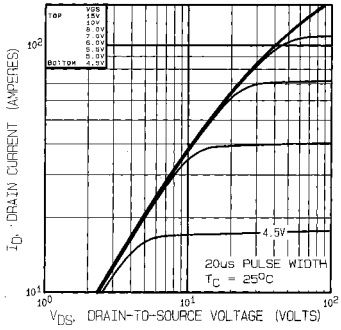


Fig 1. Typical Output Characteristics, T_C=25°C

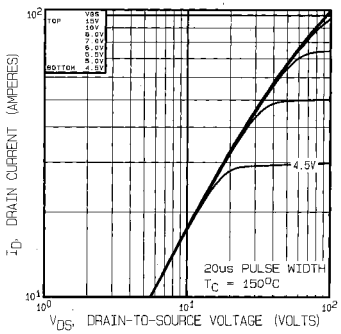


Fig 2. Typical Output Characteristics, T_C=150°C

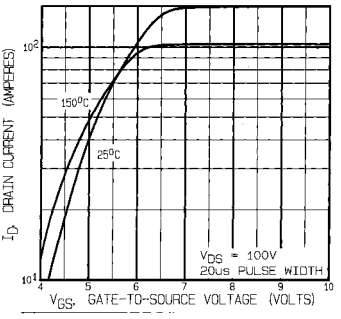


Fig 3. Typical Transfer Characteristics

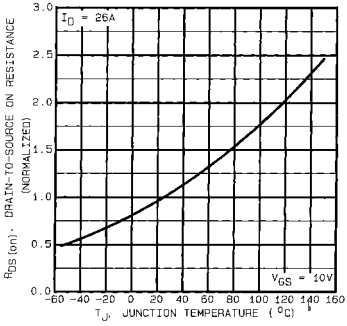


Fig 4. Normalized On-Resistance Vs. Temperature

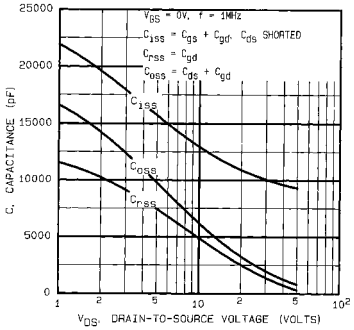


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

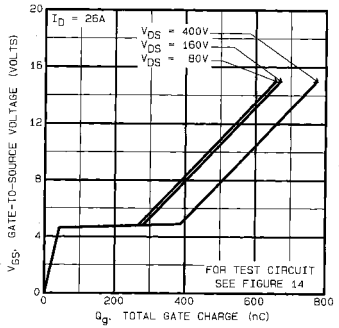


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

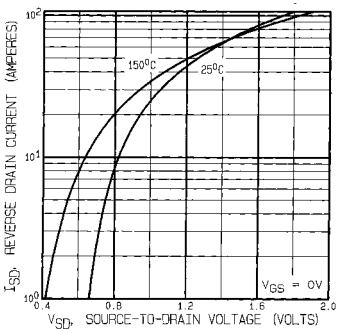


Fig 7. Typical Source-Drain Diode Forward Voltage

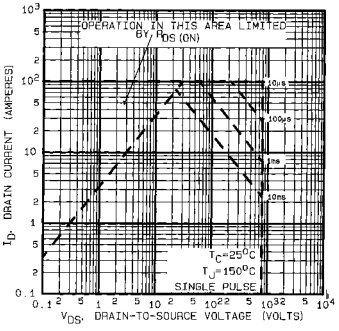


Fig 8. Maximum Safe Operating Area

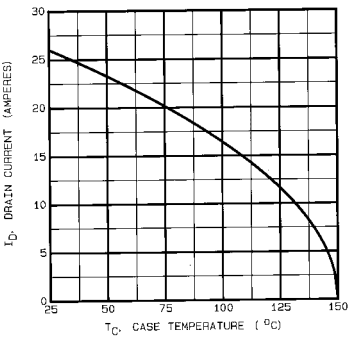


Fig 9. Maximum Drain Current Vs. Case Temperature

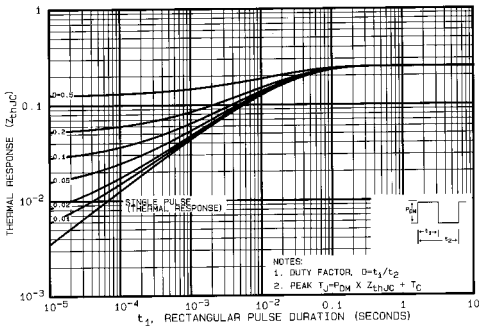


Fig 10. Maximum Effective Transient Thermal Impedance, Junction-to-Case

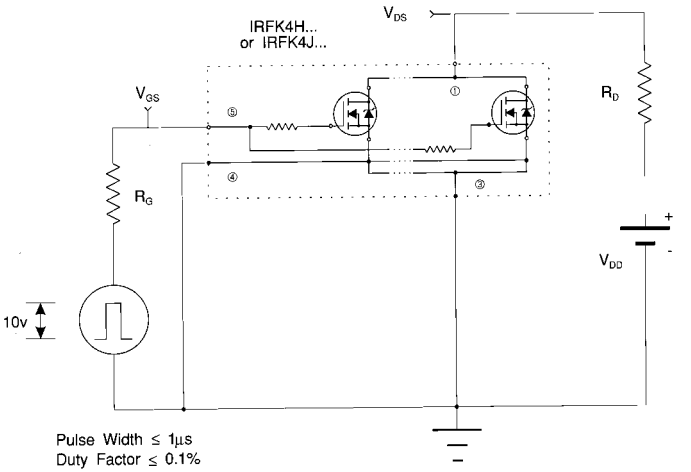


Fig 11a. Switching Time Test Circuit

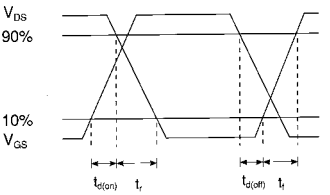
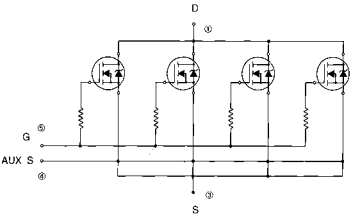
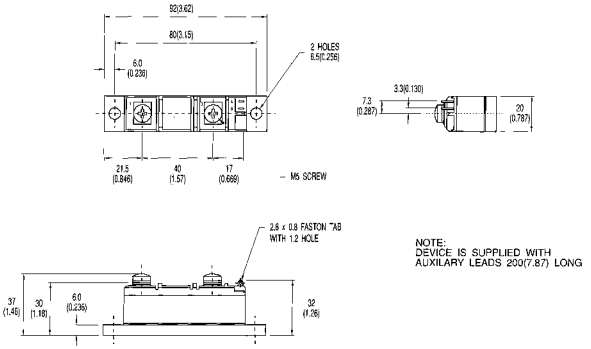


Fig 11b. Switching Time Waveforms

Circuit Configuration and Outline

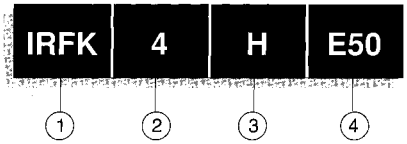




NOTE:
DEVICE IS SUPPLIED WITH
AUXILARY LEADS 200(7.87) LONG

All dimensions in millimetres (inches)

Part Numbering



1. - HEX-pak Module.
2. - Number of HEXFETs in parallel.
3. - H - Fast switching.
   - J - Oscillation resistant for sensitive applications.
4. - Voltage code:- 054 - 60V
   150 - 100V
   250 - 200V
   350 - 400V
   450 - 500V
   C50 - 600V
   E50 - 800V

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